

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Previously Presented) An apparatus for driving a flat display panel comprising a scan driving unit for controlling an upper voltage value and a lower voltage value which are applied to an Integrated Circuit (IC) for driving a scan electrode of the flat display panel,

wherein the scan driving unit comprises a timing control unit for outputting a timing control signal, an upper voltage generating unit for outputting the upper voltage value, a lower voltage generating unit for outputting the lower voltage value, and an amplifying unit for amplifying the upper voltage value applied to the scan driving unit to a predetermined level, wherein the scan driving unit selectively outputs one of the amplified upper voltage value and the lower voltage value on the basis of the timing control signal.

2. (Canceled)

3. (Previously Presented) The apparatus of claim 1, wherein the amplifying unit comprises an Operational Amplifier (OP-AMP).

4. (Canceled)

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5. (Previously Presented) The apparatus of claim 1, wherein the amplifying unit comprises an OP-AMP and a transistor (TR) connected to an output terminal of the OP-AMP.

6-7. (Canceled)

8. (Previously Presented) The apparatus of claim 1, wherein the scan driving unit further comprises switching devices having a push-pull form turned on/off on the basis of the timing control signal.

9. (Previously Presented) The apparatus of claim 8, wherein the switching devices comprise a Field Effect Transistor (FET).

10. (Currently Amended) The apparatus of claim 1, wherein the upper voltage generating unit comprises switching devices having a push-pull form turned on/off on the basis of [[the]] an upper switching control signal.

11. (Previously Presented) The apparatus of claim 10, wherein the switching devices comprise a Field Effect Transistor (FET).

12. (Currently Amended) The apparatus of claim 1, wherein the lower voltage generating unit comprises switching devices having a push-pull form turned on/off on the basis of [[the]] a lower switching control signal.

13. (Previously Presented) The apparatus of claim 12, wherein the switching devices comprise a Field Effect Transistor (FET).

14. (Previously Presented) An apparatus comprising:  
a scan driving unit to control an upper voltage value and a lower voltage value to be applied to a circuit for driving a scan electrode of a flat display panel, wherein the scan driving unit includes a timing control unit for outputting a timing control signal, an upper voltage generating unit for outputting the upper voltage value, a lower voltage generating unit for outputting the lower voltage value, and an amplifying unit for converting the upper voltage value applied to the scan driving unit to a current and amplifying the current to a predetermined level, wherein the scan driving unit selectively outputs the amplified current or the lower voltage value based on the timing control signal.

15. (Previously Presented) The apparatus of claim 14, wherein the amplifying unit comprises an operational amplifier.

16-17. (Canceled)

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18. (Previously Presented) The apparatus of claim 14, wherein the upper voltage generating unit comprises switching devices.

19. (Currently Amended) The apparatus of claim 18, wherein the switching devices are turned on/off based on ~~[[the]]~~an upper switching control signal.

20. (Previously Presented) The apparatus of claim 14, wherein the lower voltage generating unit comprises switching devices.

21. (Currently Amended) The apparatus of claim ~~[[14]]~~20, wherein the switching devices are turned on/off based on ~~[[the ]]~~a lower switching control signal.

22. (Canceled)